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## 1 Research Interests

Signal processing, computer vision, cyber-physical systems, embedded systems, machine learning, wireless communication, and wireless sensor networks.

## 2 Education

- Ph.D. in Electrical Engineering and Computer Sciences, University of California at Berkeley, 1994
- M.S. in Electrical Engineering and Computer Sciences, University of California at Berkeley, 1991
- B.S. in Electrical and Computer Engineering, and Computer Science (double major) University of Wisconsin at Madison, 1987

## 3 Academic Positions

- 2006–present: Professor, Dept. of Electrical and Computer Engineering, University of Maryland, College Park, MD (UMD).
- 1997–present: Joint Faculty Appointment, Institute for Advanced Computer Studies (UMI-ACS), UMD.
- 2021–present: Chair of Excellence in Design Methodologies and Tools (part-time), Institut National Des Sciences Appliquées (INSA), Rennes, France.
- 2019–2020: Chaire Internationale de Recherche (part-time), INSA/INRIA, Rennes, France.
- 2015–2018: Visiting Professor (part-time), Department of Pervasive Computing, Tampere University of Technology, Finland, as part of the Finland Distinguished Professor Programme (FiDiPro).
- 2001–2006: Associate Professor, Dept. of Electrical and Computer Engineering, UMD.
- 1997–2001: Assistant Professor, Dept. of Electrical and Computer Engineering, UMD.
- 1992–1994: Research Assistant, Electronics Research Laboratory, University of California, Berkeley, CA.
- 1987–1991: Research Assistant, Electronics Research Laboratory, University of California, Berkeley, CA.

## 4 Other Positions

- 2013–2022: Co Editor in Chief, Journal of Signal Processing Systems, Springer Science + Business Media, LLC, New York, NY.
- 2021, 2022: Autonomy Technology Research Center (ATRC) Mentor, Wright State University, Dayton, OH.
- 2013–2015: President and Chief Technologist, Streamlined Embedded Technologies, LLC, Ellicott City, MD.
- 2009: Visiting Faculty Research Program, Air Force Research Laboratory (AFRL), Rome, NY.
- 1994–1997: Researcher, Semiconductor Research Laboratory, Hitachi America, San Jose, CA.
- 1991–1992: Compiler Developer, Kuck & Associates, Champaign, IL.

## 5 Honors

- FiDiPro Professor, Finland Distinguished Professor Programme, TEKES, The Finnish Funding Agency for Innovation (Finland, 2015–2018).
- Jimmy Lin Award for Entrepreneurship, University of Maryland (2012).
- Fellow of the Institute of Electrical and Electronics Engineers (IEEE) “for contributions to design optimization for signal processing” (2011).
- Invention of the Year Finalist, University of Maryland (2011).
- Fulbright Specialist (Austria, 2009 & 2015 and Germany, 2005).
- Nokia Distinguished Lecturer (Finland, 2006).
- National Science Foundation CAREER Award (1998).

## 6 Publications

Author or co-author of seven books and over 300 technical papers in the areas of signal processing, embedded systems, electronic design automation, machine learning, wireless communication, and wireless sensor networks. More details are available at <https://code.umd.edu/dspcad-pub/dspcadwiki/-/wikis/Maryland-DSPCAD-Research-Group>, and <http://www.ece.umd.edu/~ssb/ssb-cv.pdf>.