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PAPERS ON INTEGRATED CIRCUIT SYNTHESIS, II

Compiled by

R. W. Newcomb and R. G. de Oliveira

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Systems Theory Laboratory
Stanford Electronics Laboratories
Stanford University Stanford, California

FOREWORD

The eight papers that constitute this report were presented as term papers by graduate students at Stanford University for the electrical engineering course EE 237, "Advanced Network Synthesis," Spring Quarter, 1967. The papers represent a continuation of the previous report, compiled under similar circumstances, "Papers on Integrated Circuit Synthesis," Stanford Electronics Laboratories, Technical Report No. 6560-4, June 1966.

The requirement for the course was to give a summary of a study in depth of some suitable topic in the field of integrated circuit synthesis; this to be done in less than 10 pages. New research results were not expected; however, as this compilation shows, some excellent new results, as well as some useful summaries, have been presented. Unfortunately, the funding situation has precluded publication of the remaining papers submitted.

Judging from the response to the previous compilation, it is felt that publication of these papers, which in a general way offer a compact survey of some present research topics, can be of help to those working in the area of integrated circuit synthesis. It should, however, be remembered that these are term papers and as such do not reflect the polished presentation required of more formal reports. A short preview of each paper is inserted for reader guidance.

The compilers are indebted to Mary Ellen Terry for her care in the final preparation and presentation; they wish to gratefully acknowledge their appreciation for her assistance on this rather difficult task.

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