

A Voltage Tunable Active-R Filter³

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ABSTRACT

By using appropriate single-pole amplifiers, realized in any of a number of forms, and the voltage variable DVCCS of Fukahori, a basic building block for tunable filters suitable for VLSI is introduced. This is used to realize voltage tunable active-R filters. A canonical second order filter with all filter coefficients voltage adjustable is presented.

I. Introduction

Previously [1] it has been shown how the technique of active-R filter design of Soderstrand [2] can be improved upon by incorporating non-zero pole locations of the amplifiers. Here we combine the technique of [1] with some improvements on the idea of Fukahori [3] to allow the design of voltage tunable active-R filters.

Fukahori has introduced a very nice circuit for realizing voltage-variable gain in an integrated circuit (IC) voltage controlled current source. This is used by Fukahori to feed an op-amp with capacitive feedback to yield a voltage-variable integrator. Here we replace the capacitive feedback with resistive feedback and use instead the amplifier pole to obtain the filter dynamics. To make the idea practical we must go beyond a simple replacement and introduce a second resistive and adjustable feedback loop to achieve the basic integrator section. This has the advantage that it will allow the construction of second order filter sections where all coefficients can be independently tuned. The technique is similar to that of Brand & Schaumann [4] but differs in that the tuning is advantageously done via Fukahoris' circuit rather than through op-amps.

II. The Basic Components

Figure 1 shows the symbolism we will use for the differential voltage-controlled current source (DVCCS) and voltage controlled voltage source (VCVS). The former follows the notation used with Bialko [5] but indicates two other controlling voltages V_T and V_O used to control the bias currents I_T and I_O in the circuit of Fukahori [3, Fig. 5]. In both parts of Fig. 1 we assume the input currents are zero while the output currents have a return to ground (omitted for simplicity of drawing). For the DVCCS, following Fukahori [3, p. 731], we can achieve

$$g_m = \frac{1}{R} \frac{I_T}{I_O} = \frac{1}{R} \frac{R_O}{R_T} \frac{V_T}{V_O} \quad (II-1)$$

for the control of g_m . Actually Fukahori sets only $I_T = V_T/R_T$, but using the same bias current generating circuit [3, Fig. 4] we can also set $I_O = V_O/R_O$, except that since two identical current sources I_O are used in the DVCCS, [3, Fig. 2], they are both to be controlled by one voltage V_O .

Concerning the VCVS, we shall assume that two types are available. One, the flat-gain type, will be such that $K(s) = -K_0$ for K_0 a real constant. The other, the single-pole type, will have

$$K(s) = -\frac{K_0 \omega_c}{s + \omega_c} \quad (II-2)$$

In practice these two types can be obtained over the frequency ranges of interest by various means, for example, in CMOS realizations by the circuits of Soderstrand [2]. Practically we will desire ω_c as small as feasible, which initiates a new kind of IC amplifier [6].

The basic integrating amplifier we use here is shown in Fig. 2. In Fig. 2 we will vary V_{Ta} for desired filter characteristics and V_{Tb} to obtain good integration. This latter is achieved by noting that for structural and Lyapunov stability we desire the integrator pole slightly in the left half plane. Thus in

$$T(s) = \frac{v_o}{v_i - v_-} = \frac{(K_0 \omega_c)(R_a g_{ma})(R_b g_{mb})}{s + \omega_c [1 + K_0(1 - R_b g_{mb})]} \quad (II-3)$$

we set

$$\epsilon = 1 + K_0(1 - R_b g_{mb}) \geq 0 \quad (II-4)$$

small; i.e. we choose, by adjusting V_{Tb} ,

$$g_{mb}(V_{Tb}) = \frac{1}{R_b} \cdot \left(\frac{K_0 + 1 - \epsilon}{K_0} \right) \quad (II-5)$$

which gives

$$T(s) = \frac{(K_0 + 1 - \epsilon) \omega_c (R_a g_{ma})}{s + \epsilon \omega_c} \quad (II-6)$$

Since $T(s)$ varies linearly with g_{ma} and the latter

varies linearly with V_{Ta} . $T(s)$ varies linearly with V_{Ta} .

Since filters need addition and subtraction, Fig. 3 shows some examples of how these can be attained with the circuit elements on hand using the flat-gain amplifier.

III. The Filter Section

The filter configuration is that of Fig. 4. This uses the state-variable configuration of Fukahori [3, Fig. 6] to form the denominator and the components of Fig. 3 to form an arbitrary (degree two) numerator. However, rather than use capacitive feedback to form the integrator, we use the active-R circuit of Fig. 2 for which we will take into account that the pole is not at zero. Thus we set, for eq. (II-6)

$$T(s) = \underline{T}(p) = \frac{k}{p} \quad (III-1a)$$

$$p = s + \alpha, \quad \alpha = \epsilon \omega_c, \quad k = (K_0 + 1 - \epsilon) \omega_c (R_a \epsilon m_a) \quad (III-1b)$$

where we note that the gain k varies with V_{Ta} but not cut-off frequency α (which, however, is set by V_{Tb}).

Given any degree two transfer function

$$H(s) = \frac{v_o}{v_i} = \frac{b_2 s^2 + b_1 s + b_0}{s^2 + a_1 s + a_0} \quad (III-2)$$

we predistort it via $s = p - \alpha$ to get

$$\underline{H}(p) = H(s) = \frac{b_2 p^2 + b_1 p + b_0}{p^2 + a_1 p + a_0} \quad (III-3a)$$

$$= \frac{b_2 p^2 + [b_1 - 2\alpha b_2] p + [b_0 - \alpha b_1 + \alpha^2 b_2]}{p^2 + [a_1 - 2\alpha] p + [a_0 - \alpha a_1 + \alpha^2]} \quad (III-3b)$$

The philosophy is then to analyze Fig. 4 in p and equate terms of the result with (III-3b) in order to realize any given $H(s)$, (III-2). Any variable coefficients in $H(s)$ will become variable coefficients in (III-3b) and hence in the realized circuit. Some realizability constraints would be met along the way, the most interesting being that \underline{a}_1 & \underline{a}_0 should be positive (which can often be achieved by V_{Tb} to give small enough α). Classically, one wishes to vary the denominator $\omega_c = \sqrt{a_0}$ without affecting its $Q = \sqrt{a_0}/a_1$, which we shall see is possible.

Setting

$$T_1 = k_1/p, \quad T_2 = k_2/p, \quad G_1 = 1/R_1, \quad G_2 = 1/R_2 \quad (III-4a)$$

$$\Delta(p) = \frac{k_1 k_2}{p^2} + c_1 \frac{k_1}{p} + 1 \quad (III-4b)$$

$$c_1 = \frac{G_1}{G_1 + G_2}, \quad c_2 = \frac{G_2}{G_1 + G_2} \quad (III-4c)$$

gives, on analyzing Fig. 4,

$$\frac{v_1}{v_i} = \frac{c_2 \left(\frac{k_1 k_2}{p^2} + 1 \right)}{\Delta}, \quad \frac{v_2}{v_i} = -c_2 \frac{k_1/p}{\Delta}$$

$$\frac{v_3}{v_i} = c_2 \frac{k_1 k_2 / p^2}{\Delta} \quad (III-5a)$$

$$\frac{v_1 - v_3}{v_i} = \frac{c_2}{\Delta} \quad (III-5b)$$

Therefore

$$\frac{v_1 - v_3}{v_i} = \frac{c_2 p^2}{p^2 + c_1 k_1 p + k_1 k_2}, \quad \frac{v_2}{v_i} = \frac{-c_2 k_1 p}{p^2 + c_1 k_1 p + k_1 k_2}$$

$$\frac{v_3}{v_i} = \frac{c_2 k_1 k_2}{p^2 + c_1 k_1 p + k_1 k_2} \quad (III-6)$$

Thus the output of Fig. 4 is

$$\frac{v_o}{v_i} = \frac{\frac{K_0}{1+K_0} c_2 R_f [\epsilon m_a p^2 - k_1 \epsilon m_a p + k_1 k_2 \epsilon m_a]}{p^2 + c_1 k_1 p + k_1 k_2} \quad (III-7)$$

Equating coefficients in (III-7) with (III-3b)

$$\underline{a}_1 = a_1 - 2\alpha = c_1 k_1 \quad (III-8a)$$

$$\underline{a}_0 = a_0 - \alpha a_1 + \alpha^2 = k_1 k_2 \quad (III-8b)$$

$$\underline{b}_2 = b_2 = \frac{K_0}{1+K_0} c_2 R_f \epsilon m_a \quad (III-8c)$$

$$\underline{b}_1 = b_1 - 2\alpha b_2 = -\frac{K_0}{1+K_0} c_2 R_f k_1 \epsilon m_a \quad (III-8d)$$

$$\underline{b}_0 = b_0 - \alpha b_1 + \alpha^2 b_2 = \frac{K_0}{1+K_0} c_2 R_f k_1 k_2 \epsilon m_a \quad (III-8e)$$

From this we see that Fig. 4 as drawn is for $b_2 > 0$, $b_1 < 0$ and $b_0 > 0$; other signs can be achieved by reversing input leads in DVCCS's.

Therefore, given a_0, a_1, b_0, b_1, b_2 we choose $\alpha = \epsilon_{m1} c_1$, by adjustment of V_{Tb} in T_1 & T_2 , such that equations (III-8) can be met (perhaps also by sign changes in the ϵ_{m1} 's). Then we solve for $k_1, k_2, \epsilon_{m1}, \epsilon_{m2}, \epsilon_{m3}$, eqs. (III-9) giving a unique solution. If any of a_0, a_1, b_0-b_2 , vary, then variations in $k_1-\epsilon_{m1}$ will be known and can be achieved by varying the appropriate V_T 's & V_0 's. For reference we have

$$k_1 = (a_1 - 2\alpha) / c_1 \quad (III-9a)$$

$$k_2 = (a_0 - \alpha a_1 + \alpha^2) / k_1 \quad (III-9b)$$

$$\epsilon_{m3} = [(1 + K_0) b_2] / [K_0 c_2 R_f] \quad (III-9d)$$

$$\epsilon_{m2} = -[(1 + K_0)(b_1 - 2\alpha b_2)] / [K_0 c_2 R_f k_1]$$

$$\epsilon_{m1} = [(1 + K_0)(b_0 - \alpha b_1 + \alpha^2 b_2)] / [K_0 c_2 R_f k_1 k_2] \quad (III-9e)$$

If complicated adjustments are desired these could be microprocessor controlled.

IV. Discussion

Here we have presented an active-R second order filter based upon the voltage variable DVCCS of Fukahori. Because this latter is designed for IC fabrication, the filter structure becomes suitable for VLSI when suitable integrators and flat-gain amplifiers are available. The latter already appears in Fukahori and the former is under development. Thus the structure allows for on chip microprocessor control of filter characteristics.

Because, for stability purposes, ideal

active-R integrators are not preferable to ones with nonzero poles, the theory has been set up to take into account nonzero poles of the integrators through predistortion. But the technique gives an added degree of flexibility in that the integrator pole position can electronically be adjusted.

Of course higher order and matrix-valued filter characteristics can be obtained by factorization of transfer functions and matrices into degree two (or one) portions [7] in which case the section presented here can serve as a universal building block for on-chip microprocessor controlled filters.

References

- [1] O.A. Seriki, G. Indrajo and R.W. Newcomb, "High-Frequency, Extended CMOS Active-R Filters", Proceedings of the Twenty-first Midwest Symposium on Circuits and Systems, Iowa State University, August 1978, pp. 174-178.
- [2] M.A. Soderstrand, "An Improved CMOS Active-R Filter", Proceedings of the IEEE, Vol. 65, No. 8, August 1977, pp. 1204-1206.
- [3] K. Fukahori, "A Bipolar Voltage-Controlled Tunable Filter", IEEE Journal of Solid-State Circuits, vol. SC-16, NO. 6, December 1981, pp. 729-737.
- [4] J.R. Brand and R. Schaumann, "A Temperature-Compensated, High-Frequency, Tunable "Active-R" Filter Using Programmable Operational Amplifiers", Proceedings of the 1975 Midwest Symposium on Circuits and Systems, pp. 603-607.
- [5] M. Bialko and R.W. Newcomb, "Generation of All Finite Linear Circuits Using the Integrated DVCCS", IEEE Transactions on Circuit Theory, Vol. CT-18, No. 6, November 1971, pp. 733-736.
- [6] K. Zaki, S.T. Liu, and R.W. Newcomb, "CAD of a voltage-Variable Active-R Integrator for Filters on VLSI Chips", manuscript in preparation.
- [7] R.W. Newcomb, "Active Integrated Circuit Synthesis", Prentice-Hall, Englewood Cliffs, NJ, 1968.

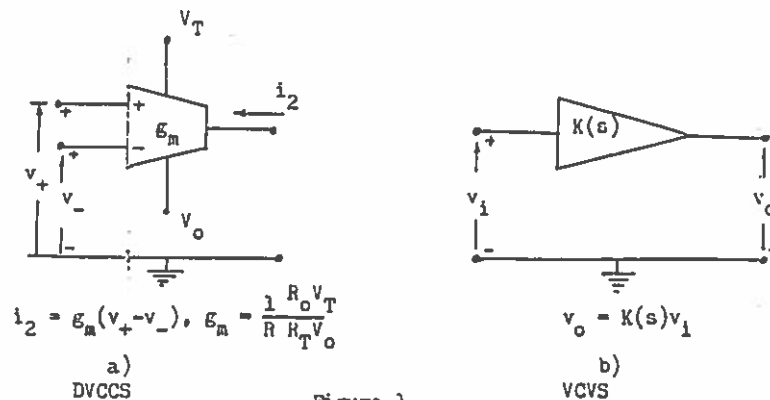


Figure 1

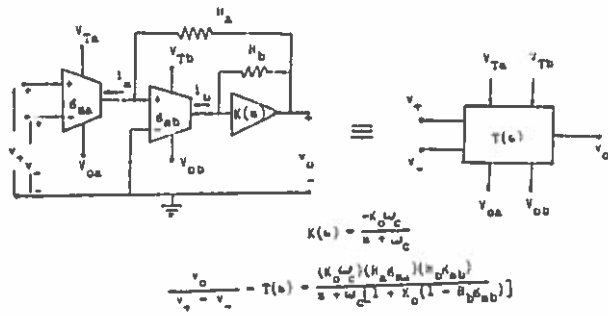


Figure 2

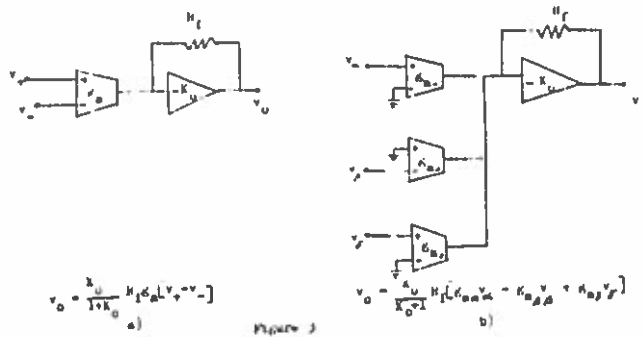


Figure 3

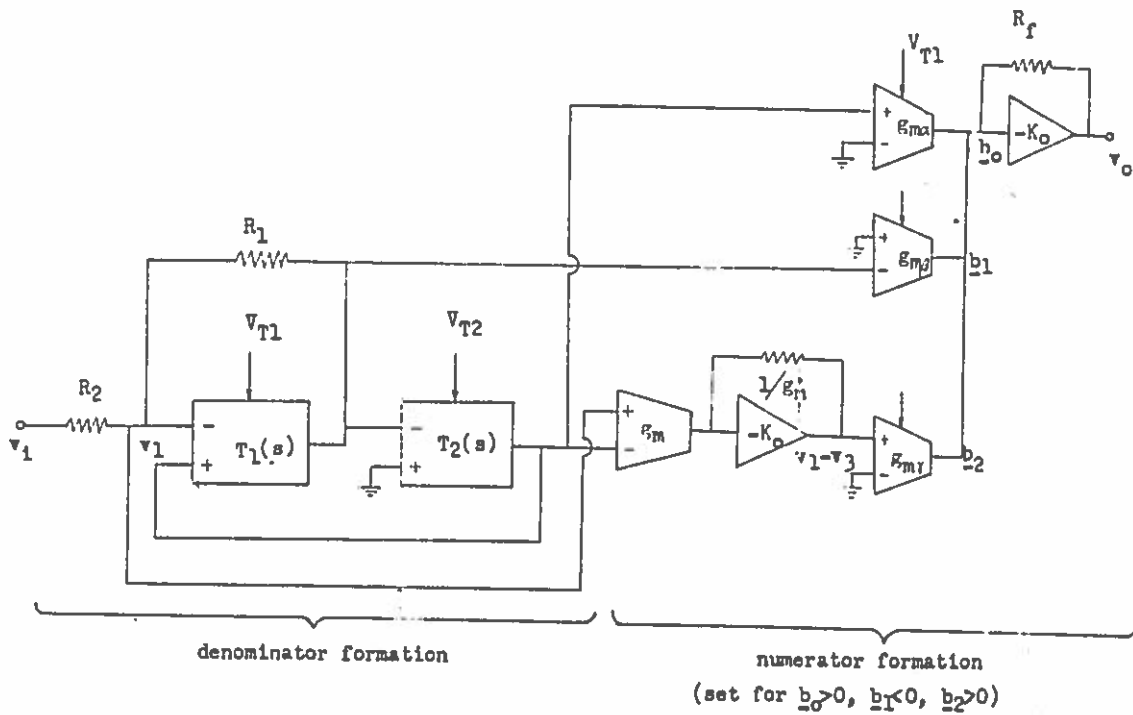


Figure 4



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QUEEN ELIZABETH HOTEL
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