TIME-INVARIANT ADJOINT NETWORKS: GENERALIZED DESCRIPTION FORMULATION

Robert W. Hewcomb and Quoc Minh On

Electrical Engineering Department University of Maryland, College Park, MD 20742

ABSTRACT

The adjoint network is developed through the allowed pair formulation of networks and shown to yield a general description representation in the time-invariant case.

I. INTRODUCTION

Since Bordewijk [1] introduced the concept of interreciprocity as applied to electrical networks, the adjoint network has played an important role in Computer Aided Design [2] and network sensitivity [3]. Its description has recently been developed in terms of general descriptions on graphs by Descer [4].

In this paper a formulation of the adjoint network N^{R} is outlined in terms of the definition [5] of an n-port network N through its allowed pairs of voltage and current, [v, i] \in N. This is developed for any linear time-invariant network having the frequency domain general description AV = BI, amplifying the idea of Descer. Moreover, the general description for the adjoint network N^{R} is found.

II. FORMLATION OF THE ADJOINT NETWORK Given an n-port network defined through its set of allowed time-domain voltage - current pairs $[v, i] \in \mathbb{N}$, [5, p.7], with v and i the n-vectors of part voltages and currents, we define the adjoint n-port network \mathbb{N}^R by $[v^R, i^R] \in \mathbb{N}^R$ if and only if for all $[v, i] \in \mathbb{N}$ and all t and τ

$$\begin{bmatrix} \tilde{\mathbf{v}}(t), \ \tilde{\mathbf{I}}(t) \end{bmatrix} \begin{bmatrix} \mathbf{i}^{\mathbf{a}}(\tau) \\ -\mathbf{v}^{\mathbf{a}}(\tau) \end{bmatrix} \approx 0 \tag{1}$$

where $\bar{}$ denotes the transpose. Let $t = x - \tau$ and

and integrate over τ to get convolution in (1). On taking Laplace transforms this gives in the frequency domain

$$\left[\tilde{V}(p), \tilde{I}(p)\right] \begin{bmatrix} I^{a}(p) \\ -V^{a}(p) \end{bmatrix} = 0$$
 (2)

If N is linear and characterized in the frequency domain by the general description

$$AV = BI \tag{3}$$

then we can similarly characterize N^{th} as follows: Equation (3) implies

$$\begin{bmatrix} A, -B \end{bmatrix} \begin{bmatrix} V \\ I \end{bmatrix} = 0_{n \times 1}$$

or equivalently on transposing

$$\begin{bmatrix} \tilde{V}_{\bullet} & \tilde{I} \end{bmatrix} \begin{bmatrix} \tilde{A} \\ -\tilde{B} \end{bmatrix} = 0_{1\times n}$$
 (4)

Multiplying by an arbitrary n-vector X, Equation (4) gives

$$\begin{bmatrix} \tilde{\mathbf{v}}_{\bullet} & \tilde{\mathbf{I}} \end{bmatrix} \begin{bmatrix} \tilde{\mathbf{A}} \\ -\tilde{\mathbf{B}} \end{bmatrix} \mathbf{X} = 0$$
 (5)

On identifying with Equation (2)

$$I^{\underline{a}} = \widetilde{A}X \tag{6a}$$

$$V^{\underline{a}} = \overline{BX}$$
 (6b)

III. GENERAL DESCRIPTION OF THE ADJOINT NETWORK

By finding a common left multiple N of A and
B for Equations (6), following [6, p.35,36] (as
illustrated by the example below), we obtain

^{*} The first author was supported in part for this research under National Science Foundation Grant ENG 75-03227.

$$\begin{bmatrix} B^{A}, A^{A} \end{bmatrix} \begin{bmatrix} \tilde{A} \\ -\tilde{B} \end{bmatrix} = 0_{\text{nxn}}$$
 (7a)

or

$$H = B^{a}\widetilde{A} = A^{a}\widetilde{B} \tag{7b}$$

where $[B^{a}, A^{a}]$ has rank n, the maximum possible rank. Thus, for these matrices B^{a} and A^{a} in p, Equations (6) give

$$B_{\mathbf{u}}\mathbf{I}_{\mathbf{u}} = B_{\mathbf{u}}\mathbf{X} = \mathbf{H}\mathbf{X} = \mathbf{A}_{\mathbf{u}}\mathbf{B}\mathbf{X} = \mathbf{A}_{\mathbf{u}}\mathbf{A}_{\mathbf{u}}. \tag{8}$$

The general description of the adjoint network \mathbb{R}^{2} is thus established as

$$B_{\mathbf{a}}I_{\mathbf{a}} = \mathbf{A}_{\mathbf{a}}\Lambda_{\mathbf{a}} \tag{9}$$

which completely describes the adjoint network in view of the rank n condition satisfied by $[B^a, A^a]$ as mentioned above.

Example

A current controlled current source has

$$\mathbf{AV} = \begin{bmatrix} 1 & 0 \\ 0 & 0 \end{bmatrix} \begin{bmatrix} \mathbf{v}_1 \\ \mathbf{v}_2 \end{bmatrix} = \begin{bmatrix} 0 & 0 \\ \beta & -1 \end{bmatrix} \begin{bmatrix} \mathbf{i}_1 \\ \mathbf{i}_2 \end{bmatrix} = \mathbf{Bi}$$

We form, with the W matrix nonsingular, using elementary row and column operations

$$\begin{bmatrix} \mathbf{W}_{11} & \mathbf{W}_{12} \\ \mathbf{W}_{21} & \mathbf{W}_{22} \end{bmatrix} \begin{bmatrix} \tilde{\mathbf{A}} & \mathbf{0} \\ -\tilde{\mathbf{B}} & \mathbf{0} \end{bmatrix} = \begin{bmatrix} \mathbf{D} & \mathbf{0}_{\mathbf{B}} \\ \mathbf{0}_{\mathbf{n}} & \mathbf{0}_{\mathbf{n}} \end{bmatrix}$$

from which

In the case of our example

or, extracting the 2x2 (2,1) submatrix.

$$\begin{bmatrix}0&0\\0&1\end{bmatrix}\begin{bmatrix}1&0\\0&0\end{bmatrix}=\begin{bmatrix}-1&-\beta\\0&0\end{bmatrix}\begin{bmatrix}0&\beta\\0&-1\end{bmatrix}$$

Multiplying by X on the right gives, from (6)

$$\begin{bmatrix} 0 & 0 \\ 0 & 1 \end{bmatrix} \begin{bmatrix} \mathbf{i}_1^{\mathbf{a}} \\ \mathbf{i}_2^{\mathbf{a}} \end{bmatrix} = \begin{bmatrix} -1 & -\beta \\ 0 & 0 \end{bmatrix} \begin{bmatrix} \mathbf{v}_1^{\mathbf{a}} \\ \mathbf{v}_2^{\mathbf{a}} \end{bmatrix}$$

which is a (reversed) voltage controlled voltage source. The circuits are as shown in Figure 1,

IV. CONCLUSIONS

We have extended in a straightforward manner the approach of Descer for formulating the adjoint network. This establishes the constraint $B^{*}I^{*}=A^{*}V^{*}$ for N^{*} directly from the port variable definition of the original network N in the frequency domain without reference to the network graph. The timevariable case will be developed elsewhere because it is too lengthy to present here. The formulation here, though, when applied to the special cases of Z, Y, and H descriptions, gives the classical results, $Z^{*}=\widetilde{Z}$, $Y^{*}=\widetilde{Y}$, and $H^{*}=\widetilde{H}$, respectively.

V. REFERENCES

- [1] J. L. Bordewijk, "Interreciprocity Applied to Electrical Network," <u>Applied Science Research</u>, V. B6, 1957, pp. 1 - 74.
- [2] S. W. Director and R. A. Rohrer, "On the Design of Resistance n-Port Networks by Digital Computer," <u>IEEE Transactions on Circuit Theory</u>, Vol. CT-16, No. 3, August 1969, pp. 337 346.
- [3] S. W. Director and R. A. Rohrer, "The General-zed Adjoint Network and Network Sensitivity,"

 <u>IEEE Transactions on Circuit Theory</u>, Vol. CT-16, No. 3, August 1969, pp. 318 323.
- [4] C. A. Descer, "On the Description of Adjoint Networks," IEEE Transactions on Circuits and Systems, Vol. CAS-22, No. 7, July 1975, pp. 585 587.
- [5] R. W. Newcomb, "Linear Multiport Synthesis,"
 McGraw-Hill Book Co., New York, 1966.
- [6] C. C. McDuffee, "The Theory of Matrices," Chelsea Publishing Co., New York, 1956.

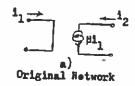


Figure 1 Example Circuita

1978 IEEE INTERNATIONAL SYMPOSIUM ON CIRCUITS AND SYSTEMS PROCEEDINGS

ROOSEVELT HOTEL NEW YORK, NY MAY 17-19, 1978



78CH1358-1 CAS