TRANSFER FUNCTION DESIGN WITH INTEGRATED CIRCUITS*

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"Artists who do not go forward go backward" [1, p. 23]

Because of their theoretical generality and their practical utility the theory of statevariables has found an important and special place within the circuit's area [2]. Here we emphasize their uses for linear integrated circuit design and construction [3] though there are equally important applications to computer aided analysis, for example.

The theory is based upon the set of first order (matrix) state-variable equations

$$\dot{x} = A x + B u \tag{1a}$$

$$y = C x + D u \tag{1b}$$

where u is the input (n-vector), y is the output (m-vector) and x is the state (k-vector); x consists of variables associated with dynamical elements, capacitor voltages say for integrated circuits; u and y for circuits would customarily be input and output voltages or currents. If the "realization" matrices A, B, C, D are constant as typically would occur for a time-invariant system, then one can find the transfer func-

 $T(s) = D + C[sl_k-A]^{-1}B, f[y] = T(s)f[u]$ (2) where $\mathbf{1}_{k}$ denotes the kxk identity matrix and $\mathcal{L}[\]$ denotes the Laplace transform and s= σ + $j\omega$ its variable. Likewise, also from (1) or a knowledge of T(s) in the form of (2), one can directly find time responses as

$$y(t) = e^{-At}y(o) + Du(t) + \int_{0}^{t} Ce^{-A(t-\tau)}Bu(\tau)d\tau$$
 (3)

This latter is especially convenient for determining transient responses and variations in transient responses (that is, sensitivity) [4] through the computer, especially when the matrix exp[-At] is evaluated by the method of Liou [5].

For actual design we wish however to recover the state-variable equations (1) from T(s) since (1) can be readily simulated in block diagram form using only integrators and weighted summers, in the standard analog simulation way. integrators and weighted summers are readily obtained with operational amplifiers and resistors and capacitors the technique is quite adaptable to integrated circuits. It turns out that when properly carried out the resulting circuits also have very low sensitivities, which lends to the method's practical attractiveness.

Let us consider the practical case of a scalar voltage transfer function T(s), n=m=1, u=v1, y=v2. In order to decrease the sensitivity (by elimination of multiple feedback paths) it is advantageous to decompose T(s) into degree one and two factors, the realizations for which can be cascaded to yield T(s); this also has the advantage of allowing each factor section to be separately impedance scaled to obtain practical element values. In actual fact the factors are conveniently obtained for many important situations from standard tables[6].

One can then give standard degree two sections from which low-pass, band-pass or highpass responses are obtained by picking signal off at appropriate points [3, p.89], while the degree one sections are easily obtained with only passive components. For illustration let us simply illustrate the theory by considering only the normalized degree-two low-pass trans-

$$\frac{\mathcal{L}\left[v_{2}\right]}{\mathcal{L}\left[v_{1}\right]} = T(s) = \frac{k}{s^{2} + 2\zeta \omega_{s} + \omega_{s}^{2}}, \quad (k, \zeta = 1/Q, \omega_{n}) \quad (4a)$$

 $\frac{\mathcal{L}\left[v_{2}\right]}{\mathcal{L}\left[v_{1}\right]} = T(s) = \frac{k}{s^{2} + 2\zeta \omega_{n} s + \omega_{n}^{2}}, \quad (k,\zeta = 1/Q,\omega_{n}) \quad (4a)$ For (1) we can obtain by standard methods [6,p.82] $A = \begin{bmatrix} 0 & -\omega_{n}^{2} \\ 1 & -2\zeta \omega_{n} \end{bmatrix}, \quad B = \begin{bmatrix} k \\ 0 \end{bmatrix}, \quad C = \begin{bmatrix} 0, 1 \end{bmatrix}, \quad D = 0$ (4b)

which is checked by (2) to give (4a). Figure 1 shows a block diagram type implementation of this "realization" which we see will use the minimum possible number of capacitors, 2, for an integrated circuit construction. Also from Fig. l we see that the Q can be made variable through the variation of one resistor, as can the ω (if we can accept a variation in Q with ω_{\perp}); such a resistor variation can of course occur conveniently by electronic means in integrated form through the use of FET devices.

Of particular practical interest for Fig. 1 are the low sensitivities obtained. For example if the gains of the integrator operational amplifiers are both K, which is generally very large, one finds for a typical integrator configuration [7, p.70]

$$S_K^{\omega} n_{\pm} = \frac{K}{\omega} \cdot \frac{\partial^{\omega} n}{\partial K} = \frac{1}{K+2}$$
 (5)

Thus, with the transfer function sensitivity, a sensitivity approaching zero can be obtained [7, p.207]. Similarly sensitivities of and to other parameters, as passive element values, are found to be relatively small [3, p.90][8, p.210].

Although there are many advantages to this

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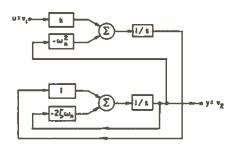
approach the main disadvantages should be mentioned. These are the frequency range, 0 to about $200 {\rm KH_Z}$, and large numbers of components required (with an attendant power consumption).

For multiple input, multiple output systems the state-variable approach comes into its own, whereas it can be also applied to distributed circuits [9] and to the design of filters by the modeling of filter sections, as that of Brune [10], to give alternate but practical results.

"The difficult must become habit, habit easy, and the easy beautiful" [1, p.23].

For J.B.:

"The most important thing is to build the life of the human spirit; therefore the goal of art is spiritual communication with people" [1, p.11].



DEGREE-TWO LOW-PASS DIAGRAM

Figure 1

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