

PAPERS ON INTEGRATED CIRCUIT SYNTHESIS, III

Compiled by

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## FOREWORD

This report constitutes the third, and probably final, collection of term papers presented by graduate students at Stanford University for the Electrical Engineering course EE 367, "Active Integrated Network Synthesis," these for the Spring Quarter 1969. The papers represent a continuation of the previous Technical Reports, No. 6560-4, June 1966 and No. 6560-13, June 1967.

As before, the main requirement for the course was to present a paper on a summary study in depth of some suitable topic in the field of integrated circuit synthesis. This was to be done in less than 15 pages (plus appendices) with an oral final based on the paper at completion. New results were not expected, though some were obtained as were some useful summaries and introductions to related topics. It should, however, be remembered that these are term papers and as such do not reflect the polished presentation required of more formal reports.

Actually, in the case of two of the papers, considerable structural editing of the original paper has occurred to present directly the main result. While all of the papers were excellent in some aspect, for various reasons, such as financial resources, timing of revisions, and proprietariness of the idea, the other seven papers have not been recorded. For those papers available, short previews have been inserted for reader guidance.

As anyone who has attempted combining the works of various authors, the job of typing and organizing the final preparation is tedious and difficult; the compilers are indebted to Betty Conder for her efforts and thoroughness in this regard and wish to deeply express their gratification, as well as to Marilyn Freal for her conscientiousness in completing the manuscript.

CONTENTS

	<u>Page</u>
I. SENSITIVITY CONSIDERATIONS IN INTEGRATED CIRCUIT SYNTHESIS (Larry McBride).....	1
A. Introduction.....	2
B. General Sensitivity.....	3
C. General High-Q Considerations.....	6
D. Some Practical Structures.....	8
E. Zero Temperature Sensitivity in Integrated Selective Amplifiers.....	13
References.....	16
II. STATE-VARIABLE SYNTHESIS OF ACTIVE RC FILTERS: A SUMMARY OF THORETICAL TECHNIQUES WITH PHYSICAL CONSTRAINTS (Howard Landsman).....	17
A. Introduction.....	18
B. The State-Variable Synthesis Technique.....	18
C. Characteristics of Real Operational Amplifiers.....	19
D. Root Locus Analysis of a Second Order Transfer Function	21
E. Tunability.....	22
F. Conclusions.....	24
Appendix	
A. Analysis of Integrator and Summer Realizations....	25
B. Sensitivity Relations for the State Variable Circuit of Kerwin, Heulsman, and Newcomb.....	28
C. Synthesis, Based on Circuit of Figure 2.....	30
D. D.C. Offset Voltage and Current Effects on State Variable Filters.....	32
E. A Tunable Active Filter.....	33
References.....	34
III. A CONSIDERATION OF THE SENSITIVITY OF THE TRANSFER FUNCTION OF AN ACTIVE FILTER SYNTHESIZED VIA STATE SPACE THEORY (Michael Frankel).....	35
A. Introduction.....	36
B. Purpose.....	36
C. State Space Synthesis.....	36

D.	Sensitivity $S_x^{T(p)}$ .....	40
E.	Conclusion.....	43
Appendix		
A.	Identity of Sensitivities.....	46
B.	Sensitivities for $T(p,x) = f(x)T(p)$ .....	48
	References.....	49
IV.	DIGITAL FILTERS (William N. Lee).....	50
A.	Introduction.....	51
B.	Digital Filter Operation.....	52
C.	Amplitude and Phase Characteristics.....	55
D.	Discussion of Some Digital Filter Design Techniques..	59
1.	Impulse Invariance Technique.....	59
2.	Digital Filter Specification from Squared Magni- tude Function.....	60
3.	Design of Digital Filters Using Bilinear Transfor- mation of Continuous Filter Functions.....	63
E.	Summary.....	66
	References.....	67
V.	ANALOG AND DIGITAL DEGREE TWO NOTCH FILTERS SUITABLE FOR INTEGRATION(John W. Craig).....	68
A.	Introduction.....	69
B.	Yanagisawa Synthesis.....	70
C.	State-Variable Synthesis.....	72
D.	The Standard -z and w-Transform Synthesis.....	77
E.	Comparison of the State-Variable and w-Transform Syn- thesis.....	84
F.	Conclusion.....	84
	Appendix.....	86
	References.....	87
VI.	GYRATOR SIMULATION OF FLOATING INDUCTANCE (P. T. Mingle).....	88
	References.....	96