

PAPERS ON ALGORITHMIC CIRCUIT STUDIES

Compiled by

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FOREWORD

The four papers presented in this report were submitted by graduate students in Electrical Engineering at Stanford University doing independent study at a beginning level, either within the framework of the course EE 266 ("Introduction to Network Synthesis", Autumn Quarter 1968) or EE 391 ("Special Studies and Reports in Electrical Engineering", Autumn and Winter Quarters 1968-69).

Because there resulted a collection of papers of general value to the community interested in computer-aided circuit studies, it was agreed upon to compile the resulting papers, on some editing, in a report. It should be remembered, therefore, that these papers are term papers and do not represent polished formal reports.

The requirement for a paper to be included was that some topic applicable to computer-aided network analysis or synthesis be either summarized or developed in compact form. Indeed, the compilation does contain useful summary material along with several unexpected significant results in the area of algorithmic structures for computer-aided circuit analysis and synthesis. Consequently, it is hoped that these presentations will be useful, as well as a stimulation to future contributions, for those working in the computer-aided area. A short preview of each paper is inserted for reader guidance.

The compilers would like to express their appreciation to Betty Conder for her initiative and care in preparation and organization of a rather difficult work.

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