

ENEE 302 Final Study Points

The exam is at 10:30 on **Saturday May 14** and will be open book open notes and will have three questions which may include material on the following topics:

- a) FET models and Spice conventions and models
- b) CMOS and BJT current sources and mirrors, both sink and source type and inverters
- b) XOR and current mode gates and their use
- c) Inverters and their characteristics
- d) Questions associated with a paper presented and chosen from the following
 - Winner-TakeAll Networks of O(N) Complexity
 - A 1.76-GHz 22.6-mW $\Delta\Sigma$ Fractional-N Frequency Synthesizer
 - A Symmetric CMOS NOR Gate for High-Speed Applications

Previous final exams with some solutions are on the web under previous offerings.

Notebooks are due at the end of the final.

For any updates check the class web page before Friday afternoon.