PSpice setup to get PMOS and NMOS curves together

setup: DC, 0<=Vdd<=5V in 0.01V steps
nest with 0<=Vgate<=5V in 1V steps
Can also add parametric run over Wp
PSpice setup for CMOS inverter curves

PARAMETERS:
\[ Wp \quad 3u \]

setup: DC, \(-5v<=Vin<=5v\) in 0.01v steps
parametric run over \( Wp \)
use 9u<=Wp<=13u in 2u steps