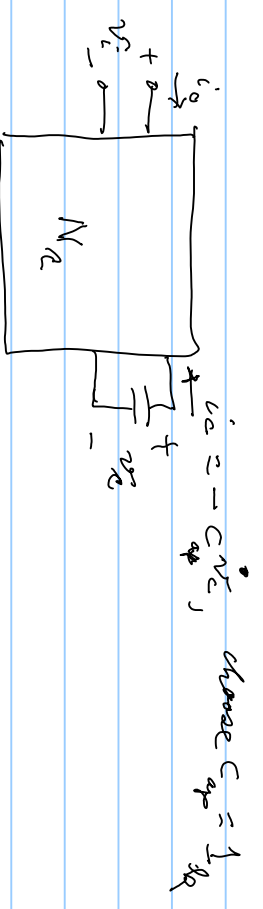
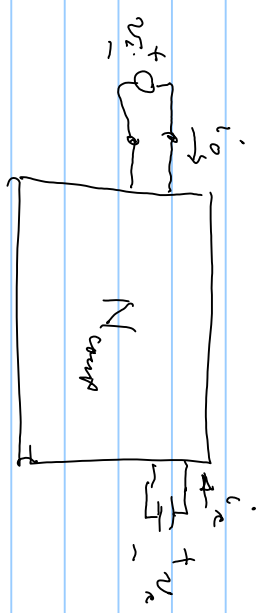


$$\dot{x} = Ax + Bu \Rightarrow v_2 = Av_2 + Bv_1$$

$$y = Cx + Du \quad i_0 = Cv_2 + Dv_1$$



$$-v_2 = \begin{bmatrix} i_0 \\ v_2 \end{bmatrix} = Y_{comp} \begin{bmatrix} v_1 \\ v_1 \end{bmatrix} = \begin{bmatrix} -A & -B \\ C & D \end{bmatrix} \begin{bmatrix} v_1 \\ v_1 \end{bmatrix}$$

$Y_{comp} = \begin{bmatrix} -A & B \\ C & D \end{bmatrix}$ a constant matrix

if A, B, C, D are constant $\Rightarrow Y_{comp} = \frac{1}{2} (Y_{comp} + Y_{comp}^T) + \frac{1}{2} (Y_{comp} - Y_{comp}^T)$

use OTA's gyrators

if $x = P\hat{x}$, P^{-1} existung

$$\dot{x} = P\dot{\hat{x}} = AP\hat{x} + Bv$$
$$y = CP\hat{x} + Du$$

$$\dot{\hat{x}} = P^{-1}AP\hat{x} + P^{-1}Bv$$
$$y = CP\hat{x} + Du$$

there exists a P to make $\hat{Y}_{comp} = \frac{\hat{Y} + \hat{Y}^T}{2}$

is positive semi-definite

positive-real matrix

by the parallel-real

lemma



if $v_0 = y$ at different ports then $u = v_0$:

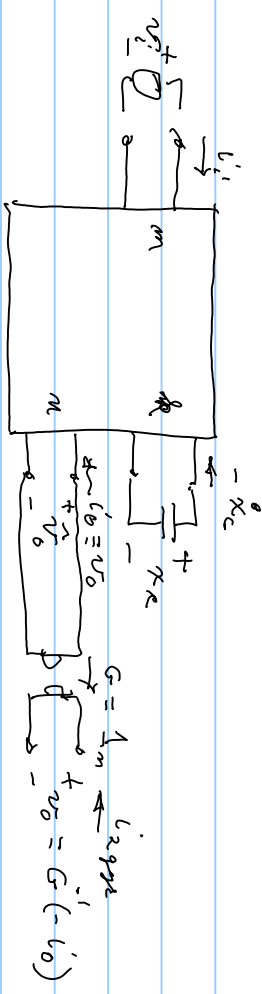
$$\hat{x}_0 = Ax_0 + Bv_0$$

$$v_0 = B^T x_0 + Dv_0$$

use algebraic

Kricak's equations

to find P



B is $k \times m$

$i_1 = u$ is an m vector

x should be $m \times k$

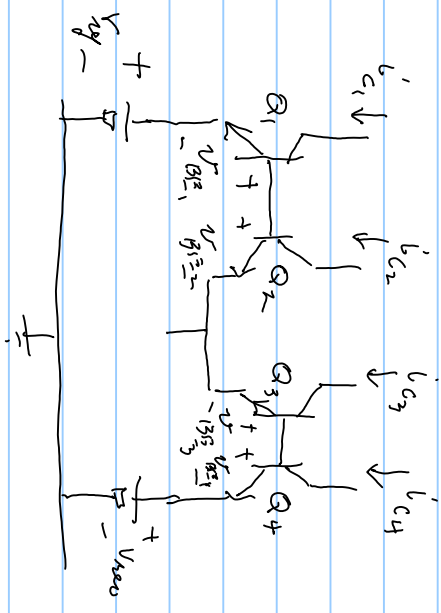
a nice choice is B^T or

the m & k roots for B can be made with systems

$$\begin{bmatrix} \dot{i}_1 \\ \dot{i}_2 \\ \dot{i}_0 \end{bmatrix} = \begin{bmatrix} -A & -B & x_{13} \\ x_{21} & x_{22} & x_{23} \\ C & D & x_{33} \end{bmatrix} \begin{bmatrix} u_c = x_c \\ u_r \\ u_0 \end{bmatrix} = 0 \text{ if no load on the gyro } (i_1 = 0, i_2 = -G \dot{u}_0)$$

can choose $x_{13} = -C^T$, $x_{23} = -D^T$, $x_{21} = 0$, $x_{33} = 0$ or skew symmetric $\rightarrow i_1 = 0$

Translinear Loop



by KVL

$$0 = -V_{top} - V_{BE1} + V_{BE2} - V_{BE3} + V_{BE4} + V_{bottom}$$

$$V_{BE1} + V_{BE3} = V_{BE2} + V_{BE4}$$

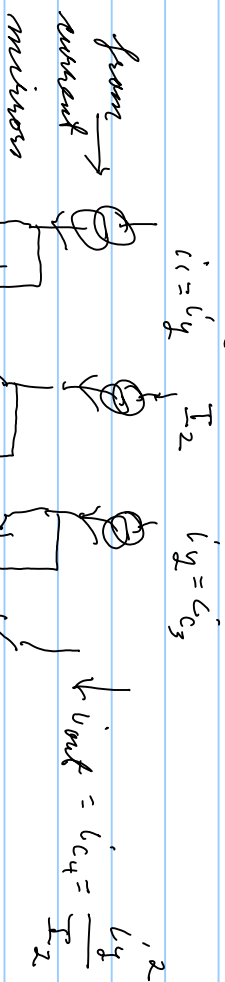
$$V_T \left(\ln \frac{i'_{c1}}{I_{01}} + \ln \frac{i'_{c3}}{I_{03}} \right) = V_T \left(\ln \frac{i'_{c2}}{I_{02}} + \ln \frac{i'_{c4}}{I_{04}} \right)$$

$$\ln \left(\frac{i'_{c1} i'_{c3}}{I_{01} I_{03}} \right) = \ln \left(\frac{i'_{c2} i'_{c4}}{I_{02} I_{04}} \right)$$

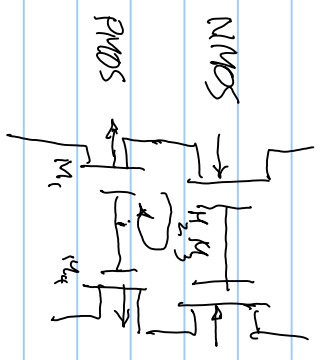
$$i'_{c1} i'_{c3} = i'_{c2} i'_{c4} \left[\frac{I_{01} I_{03}}{I_{02} I_{04}} \right]$$

$$i_D = I_D \left(e^{V_D/V_T} - 1 \right) \approx I_D e^{V_D/V_T} \Rightarrow V_D = V_T \ln \left(\frac{i_D}{I_D} \right)$$

$i_{c1} = \frac{i_{c2} i_{c4}}{i_{c3}}$, gives a means for get the product of two positive resistors



for MOS



$$0 = N_{GS1} - N_{GS2} + N_{GS3} - N_{GS4}$$

gives translinear MOS loop