

ENEE 303H Fall 2017 11/10/17 Paper Choices			
Name/Paper	Chosen Watch Topic Presentation #	presentation Dates	Commentating Dates
Brown, Matthew Nathan	Watch = Mother Board Circuit	Watch = #9 Papers: 1 st = Tu 11/14/17 m) 2 nd = Tu 11/28/17 e)	Tu 11/30/17 e)
P. L. Clood, "ABasic Transistor Circuit for the Construction of Digital-Computing Systems," The Proceedings of the Institution of Electrical Engineers, Vol. 105, Part B, No. 21, May 1958, pp. 213 - 219. [changed to above 11/10/17] Takashi Ohsawa, Katsuyuki Fujita, Tomoki Higashi, Yoshihisa Iwata, TakeshiKajiyama, Yoshiaki Asao, Kazumasa Sunouchi, "Memory Design Using One-Transistor Gain Cell on SOI," Proceedings of the ISSCC 2002, pp. 9.1 - 9.3.			
Campbell, Matthew O	Watch = Power Supply Circuits	Watch = #8 Papers: 1 st = Tu 11/14/17 g) 2 nd = Th 11/30/17 b)	Th 11/30/17 d)
Kuo-Hsing Cheng, Chi-Che Chen and Chun-Fu Chung, "Accurate Current Mirror with High Output Impedance," IEEE epaper, 2001, p. 565 - 568.			
Doubleday, Kyle David	Watch = Acceleration Sensor	Watch = #5 Papers: 1 st = Th 11/09/17 a) 2 nd = Tu 11/21/17 a)	Th 11/30/17 b)
Saam Iranmanesh, and Esther Rodriguez Villegas, "A 950 nW Analog-Based Data Reduction Chip for Wearable EEG Systems in Epilepsy," IEEE Journal of Solid-State Circuits, Vol. 52, No. 9, September 2017, pp. 2362 - 2373.			
Githinji, Morgan Kigundu	Watch = Temperature Sensor	Watch = #10 Papers: 1 st = Tu 11/14/17 h) 2 nd = Tu 11/21/17 d)	Th 11/30/17 a)
R. Shabanpour, T. Meister, K. Ishida, B. Boroujeni, C. Carta, and F. Ellinger, "A Transistor Model for a-IGZO TFT Circuit Design Built upon the ROI-aTFT Model," IEEE e-paper, 2017, pp. 129 - 131.			
Linko, John Thomas	Watch = Microcontroller Block Diagram	Watch = #11 Papers: 1 st = Tu 11/14/17 k) 2 nd = Tu 11/28/17 a)	Tu 11/28/17 d)
R. R. Torrance, T. R. Viswanathan, and J. V. Hanson, "CMOS Voltage to Current Transducers," IEEE Transactions on Circuits and Systems, Vol. CAS-32, No. 11, November 1985, pp. 1097 - 1104.			
McMenamin, Andrew Patrick	Watch = Quartz clock Oscillators	Watch = #3b Papers: 1 st =Th 11/09/17 b) RWN 2 nd = Tu 11/21/17 b)	Th 11/21/17 d)
Hiroaki Ikeda, Ma Huail-Jian and Gurishi Yamamoto, "A Nonlinear Method to Estimate Model Parameters of Junction Field-Effect Transistors," IEEE Transactions on Instrumentation and Measurement, Vol. IM-35, No. 3, September 1985, pp. 272 - 277.			
Parikh, Priyang	Watch = Microcontroller - chip/circuitry (RWN)	Watch = #12 Paper 1 st =Th 11/09/17 c) RWN 2 nd = Tu 11/21/17 c) RWN	Tu 11/21/17 b) RWN

Paul Cooper, "The W.S. Army Signal Corps' "Dick Tracy" Transistor Wrist Radio (1953)," Proceedins of the IEEE, Vol. 86, No. 1, . 163 - 169. January, pp			
Pfeiffer, Stephen Mark	Watch = LCD Driver	Watch = #1 Papers: 1 st = Th 11/16/17 f) 2 nd = Tu 11/28/17 d)	Tu 11/28/16 a)
Jyh-Ming Wang, Sung-Chuan Fang, and Wu-Shung Feng, "New Efficient Designs for XOR and XNOR Functions on the Transistor Level," IEEE Journal of Solid-State Circuits, Vol. 29, No. 7, July 1994, pp. 780 - 786.			
Roberts, Mitchell Ryan	Watch = Pressure Sensor	Watch = #4 Papers: 1 st = Th 11/16/17 i) 2 nd = Th 11/30/17 d)	Tu 11/28/17 c)
Wei Shu and Joseph Chang, "IMD of Closed-Loop Filterless Class D Amplifiers," IEEE Transactions on Circuits and Systems - I, Vol. 57, No. 2, February 2010, pp. 518 - 527.			
Andrew Tran	Watch = Buzzer	Watch = #7 Papers: 1 st = Th 11/16/17 c) 2 nd = Th 11/30/17 c)	Tu 11/28/17 e)
Qiuting Huang, Francesco Piazza, Paolo Orsatti, and Tatsuya Ohguro," The Impact of zScaling down to Deep Submicron on CMOS RF Circuits," IEEE Journal of Solid-Dtate Circuits, Vol. 33, No. 7, July 1998, pp. 1023 - 1036			
Verma, Shawn	Watch = LCD display	Watch = #2 Papers: 1 st = Th 11/16/17 h) 2 nd - Th 11/30/17 e)	Th 11/30/17 c)
Robert J.ull On-Chip CMOS Low-Dropout Voltage Regulator," IEEE Transactions on Circuts ..., Vol. 54, No9, ????. 20 pp.			
Whittle, Derek Matthew	Watch = Access Point Circuits	Watch = #13 Papers: 1 st = Th 11/09/17 d0 RWN 2 nd = Tu 11/28/17 c)	Tu 11/28/17 b)
R. A. Henle, "A Multistable Transistor Circuit," Electrical Engineering, ????, pp. 570 - 572.			
Yang, Pei-Jo	Watch = Antenna	Watch = #6 Papers: 1 st = Th 11/16/17 j) 2 nd = Th 11/30/17 a)	Tu 11/21/17 c) RWN
Luiz Alberto Pasini Melek, Anselmo Luis da Silva, Jr., Marcio Cherem Scjneider, and Carlos Galup-Montoro, "Analysis and Design of the Classical CMOS Schmitt Trigger in Subthreshold Operaion," IEEE Transactions on Circuits and Systems - I, Vol. 64, No. 4, April, 2017, pp. 869 - 878.			
Yoe, James Philip	Watch = clock oscillator circuit	Watch = #3a Papers: 1 st = Th 11/09/17 e) RWN 2 nd = Tu 11/28/17 b)	Tu 11/21/17 a) RWN
Sanu K. Matthew, David Johnson, Sudhir Satpathy, Vikram Suresh, Paul Newman, Mark A. Anders, Himanshu Kaul, Amit Agarwal, Steven K. Hsu, Gregory Chen and Ram K. Krishnamurthy, "uRing: A 300-950 mV, 323 Gbps/W All-Digital Full-Entropy True Random Number Generator in 14 nm FinFET CMOS," IEEE Journal of Solid-State Circuits, Vol. 51, No. 7, July 2016, pp. 1695 - 1704.			

Number of students = 14