

ENEE 303_2 Fall 2017 11/08/17			
Paper Choices			
Name/Paper		presentation Dates	Commentati ng dates
Danoff, Alex Aaron		Th 11/09/17 I) RWN	
Hamid R. Rategh, Hiran Samavati, and Thomas H. Lee, "A 5GHz, 1mW CMOS Voltage Controoled Differential Injection Locked Frequency Divider," ???			
Diallo, Mamadou Mouctar		Th 11/16/17 m)	
Fangyuan Dang, Yuon Wang, Yuequan Liu, Song Jia, and Xing Zhang, "Design on Multi-bit Adder Using Sense Amplifier-Based Pass Transistor Logic for Near-Threshold Voltage Operation," IEEE e-paper, 2015, 4 pages.			
Joy, Matthew Perry		Th 11/09/17 j)	
Bahram Fotouh, "All-MOS Voltage-to-Current Converter," IEEE JOURNAL OF SOLID-STATE CIRCUITS, VOL. 36, NO. 1, JANUARY 2001, pp. 147 – 151. [switched to above 11/08/17] Hao-Ping Hong and Jin-Chuan Wu, "A Reverse-Voltage Protection Circuit for MOSFET Power Switches," IEEE Journal of Solid-State Circuits, Vol. 36, No. 1, January ???, pp. ???			
Lehr, Justin Reed		Th 11/16/17 g)	
Reid R. Harrison and Cameron Charles, "A Low-Power Low-Noise CMOS Amplifier for Neural Recording Applications," IEEE Journal of Solid-State Circuits, Vol. 38, No. 6, June 2003, pp. 958 – 965.			
McKennirey, David James		Th 11/09/17 m)RWN	
Mahmoud H. Ouda, Waleed Khali, and Khaled N. Salama, "Wide-Range Adaptive RF-to-DC Power Converter for UHD RFIDs," IEEE Microwave and Wireless Components Letters, Vol. 26, No. 8, August 2016, pp. 634 – 636.			
Meredith, Jeremy Brigham		Th 11/09/17 n)RWN	
Chih-Lung Lin, Fu-Hsing Chen, Chia-Che Hung, Po-Syum Chen, Ming-Yang Deng, Chum-Ming u, and Tzuen-His Huang, "New a-IGZO Pixel Circuit Composed of Three Transistor and One Capacitor for Use in High-Speed AMOLED Displays," Journal of Display Technology, Vol. 11, No. 12, December 2015, PP. 1031 – 1034.			
Putreddi, Vinay Reddy		Th 11/09/17 o)RWN	
Reddy, Anoosh		Tu 11/14/17 c)RWN	
Hagen Marien, Michael s. J. Steyaert, Erik van Veenendaal and Paul Heremans, "Analog Building Blocks for Organic Smart Sensor Systems in Organic Thin-Film Transistor Technology on Flexible Plastic Foil," IEEE Journal of Solid-State Circuits, Vol. 47, No. 7, July 2012, pp. 3712 – 1720.			
Roshdieh, Nick Rustin		Th 11/16/17 p)	
[recd 10/13/17] Sachidanand Shukla, "Novel Circuit of Small Signal Amplifier with BJT-MOSFET Hybrid Unit in Sziklai Pair Topology," 2015 Radio and Antenna Days of the Indian Ocean, Belle Mare, 2015, pp. 1-2			
Scherr, Benjamin Charles		Tu 11/14/17 d)RWN	

Xing-Heng Xia, Wei-Jing Wu, Guan-Ming Li, Lei Zhou, Li-Rung Zhang, Hong-Long Ning, Miao Xu, Lei Wang, Jun-Biao Peng, “A new compensation pixel circuit with metal oxide thin-film transistors for active-matrix organic light-emitting diode displays,” Journal of the SID, Vol. 23, No. 6, 2015, pp. 233 – 237.			
Schuyler, Matthew Francis		Tu 11/14/17 b)	
Ramon Gonzalez Carvajal, Jaime Ramirez-Angulo, Alfonso Carlosena, and Fernando Munoz Chavero, “The Flipped Voltage Follower: A Useful Cell for Low-Voltage Low-Power Circuit Design,” IEEE Transactions on Circuits and Systems – I, Vol. 52, No. 7, July 2005, pp. 1276 – 1291.			
Sewell, Cameron Guy		Tu 11/14/17 j)RWN	
J. B. Earnshaw and P. M. Fenwick, “A Transistor-Diode Feedback Type Logic Circuit,” The Radio and Electronic Engineer, September 1966, pp. 191 – 200.			
Taleb, Rashid		Tu 11/14/17 e)RWN	
Wali, Shah-Ameer		Tu 11/14/17i)RW?N	
Charles`S. Bamji, Patricia Conner, Tamer Elkhatib, Swati Mehta, Barry Thompson, Lawrence A. Prather, and Dane Snow, “A 0.13 um CMOS System-on-Chip for a 512x424 Time-of-Flight Image Sensor up to 130MHz and 2 GS/s ADC,” IEEE Journal of Solid-State Circuits, Vol. 50, No. 1, pp. 303 – 319.			
Wei, Chenglin		Tu 11/14/17 o)	
Reid R. Harrison, and Cameron Charles, “A Low-Power Low-Noise CMOS Amplifier for Neural Recording Applications,” IEEE Journal of Solid-State Circuits,” Vol. 38, No. 6, June 2003, pp. 958 - 955			

Number of students = 15