

ENEE 303_1 Fall 2017 11/16/17			
Paper Choices			
Name/Paper		Dates	Commentating dates
Baldwin, Kacy Alec		Tu 11/14/17 p)	
Yining Zhang, Ranran Zhou, Woogeun Rhee, and Zhihua, Wang, "A 1.9-mW 750-kb/s 2.4-GHz F-OOK Transmitter With Symmetric FM Template and High-Point Modulation PLL," IEEE Journal of Solid-State Circuits, IEEE e-journal, 2017, pp. 1 - 9.			
Gonzales, Matthew James		Th 11/16/17 l)	
Takeji Fujibayashi, Yohsuke Takeda, Weihua Wang, Yi-Shin Yeh, Willem Stapelbroek, sei-ji Takeuchi, and Brian Floyd, "A 76- to 81-GHz Multi-Channel Radar Transceiver," IEEE Journal of Solid-State Circuits, Vol. 52, No. 9, September 2017, pp 2226 - 2241.			
Gramzinski, Katrina Loretta		Th 11/16/17 b)	
T. Matsumoto, L. o. Chua, and K. Tokumasu, "Double Scroll Via a Two Transistor Circuit," IEEE Transactions on Circuits and Systems, Vo. CAS-33, No. 8, August 1986, pp. 828 - 835. [changed to above 11/16/17] Yan Li,, Jerry Lopez, Cliff Schecht, Ruili Wu, and Donald Y. C. Lie, "Design of High Efficiency Monolithic Power Amplifier with Envelope-Tracking and Transistor Resizing for Broadband Wireless Applications," Vol. 47, No. 9, September 2012, pp. 2007 - 2018.			
Hornak, Brian James		Th 11/09/17 f) RWN	
[11/07/17]\ Nichi Bansa & Rishikesh Pandey, "A Novel Current Subtractor Based on Modified Wilson Current Mirror Using PMOS Transistors," Proceedings of the Micro-Electronics and Telecommunications Engineering International Conference, 2016 via IEEE enet, pp. 444-449.			
Karmarkar, Renooka Deepak		Th 11/09/17 k)	
Majid Eslami Farsani and Noshin Ghaderi, "Reduction of coupling capacitance, using a capacitor-transistor coupling circuit,???" pp. 106 - 109			
Khalaf, Ziad		Th 11/09/17 g) RWN	
[10/08/17] Wei Li, "A Transistor-Only High-Pass Filter with Adjustable Q Factor," IEEE Transactions on Circuits and Systems - I, Vol. 40, No. 2, February 1993, pp. 136 - 140.			
Kumar, Atharv		Tu 11/14/17 l)	
O. E. Osinowo and A. G. Raji, "Switch Mode-Based Constant Voltage Battery Charger for New Generation Motor Vehicles," Proceedings of the 2013 IEEE International Conference on Emerging & Sustainable Technologies for Power & ICT in a Developing Society, pp 30 - 36.			
Landis, Stuart James		Th 11/16/17 e)	
Bert Serneels, Tim Piessens, Michiel Steyaert, and Wim Dejaeme, "A High-Voltage Output Driver in a 2.5-V 0.25-um CMOS Technology," IEEE Journal on Solid-State Circuits, Vol. 40, No. 3, March 2005, pp. 576 - 583.			
Link, Stephen Christopher		Tu 11/09/17 h) RWN	

Chin-Lung Lin, Fu-Hsing Chen, Chia-Che Hung, Po-Syun Chen, Ming-Yang Deng, Chun-Ming Lu, and Tzuen-His Huang, "New a-IGZO Pixel Circuit Composed of Three Transistors and One Capacitor for Use in High-Speed-Scan AMOLED Displays," Journal of Display Technology, Vol. 11, No. 12, October 2015, pp. 1031 - 1034.			
[replaced 09/14/17]R. R. Torraance, t. R. Viswanathan, and U. V. Hanson, "MOS Voltage to Current Transducers," IEEE Transactions on Circuits and Systems, vol. CAS-32, No. 11, November 1985, pp. 1097 - 1104.			
Miller, Grayson Marie		Th 11/16/17 a)	
Liao u, Xuan-Dien Do, Sang-Gug Lee and Dong Sam Ha, "A Self-Powered and Optimal SSHI Circuit Integrated with an Active Rectifier for Piezoelectric Energy Harvesting," IEEE Transactions on Circuits and Systems - I, Vol. 64, No. 3, March 2017, pp. 537 - 549.			
Moscucci, Matteo		Th 11/16/17 n)	
M. S. Ghausi, "Optimum Design of the Shunt-Seeris Feedback Pair with a Maximally Flat Magnitude Response," IRE Transactions on Circuit Theory, December 1961, pp. 448 - 453. [replaced by above 11/06/17] Marcelo DeSouza, Andre Mariano, and Thierry Taris, "Reconfigurable Inductorless Wideband CMOS LNA for Wireless Communications," IEEE E-paper, 2016, 11 pages.			
Pashai, Naseem		Th 11/09/17 l)	
A. Bansal, M. Kumarasamy Raja, and Je Minkyu, "A 60nW Voltage Reference Circuit Generating 1.0 using BJTs and Subthreshold MOSFET," IEEE e-paper, 2012, pp. 113 - 115.			
Ray, Randolph Shadrack		Th 11/16/17 d)	
Kwang-Jow Gan, Zheng-Jie Jiang, Din-Yuen Chan, Cher-Shitiung Tsai, Jian-Syong Huang, Zhen-Jai Kao, and Wen-Kuan Yeh, "JDesign and Application of VanDer Pol Oscillator Using NDR Circuit," Proceedings of the IEEE 2 nd International Symposium on Next-Generation Electronics, pp. 329 - 332.			
Russotto, James Paul		Th 11/16/17 k)	
Escid Hammoudi and Attari Mokhtar, "Low Noise and High Bandwidth 0.35um CMOS Transimpedance Amplifier," Proceedings of the 2009 International Conference on Microelectronics, pp. 26 - 29.			
Stringer, Ian Jackson		Tu 11/14/17 a)	
Barrie Gilbert, "A Precise Four-Quadrant Multiplier wit Subnanosecond Response," IEEE Journal of Solid-State Circuits, Vol. sc-3, No. 4, December 1968, pp. 365 - 373.			
Tapscott, Elliot Payton		Th 11/16/17 o)	
Rajeee Kumar and Vimal Kant Pandrey, "A New 5-Transistor XOR-XNOR Circuit Based on the Pass Transistor Logic," IEEE epaper 2001, pp. 667 - 671.			
Weddle, Tyler Christopher		Tu 11/14/17 n)	
B. Leung, "BiCMOS Current Cell and Switch for Digital-Analog Converters," IEEE Journal of Solid-State Circuits, Vol. 28, NO. 1, January 1993, pp. 68 -71. [replaced by the above 11/07/17]Jun Won Jung and Behzad Razavi, "25-Gb/s 5-mW CMOS CDR/Deserializer," IEEE Journal of Sold-State Circuits, Vol. 48, No. 3, March 2013, pp. 684 - 697.			

Number of students = 17