

ENEE 610 F02 paper choices 10/31/05

Allnutt, Jeffrey Foster jallnutt@wam.umd.edu

C. W. Mangelsdorf, "A 400-MHz Input Flash Converter with Error Correction," IEEE Journal of Solid-State Circuits, Vol. 25, No. 1, February 1990, pp. 184 - 191.

Asawaplungkul, Suphattra suphattraa777@yahoo.com

H. Huang and E. K. F. Lee, "Design of Low-voltage CMOS Continuous-Time Filter with On-Chip Automatic Tuning," IEEE Journal of Solid-State Circuits, Vol. 36, No. 8, August 2001, pp. 1168 - 1177.

Badr, Celine George cbadr@mail.umd.edu

K. A. Kouznetsov and R. G. Meyer, "Phase Noise in LC Oscillators," IEEE Journal of Solid-State Circuits, Vol. 35, No. 8, August 2000, pp. 1244 - 1248.

Chiang, Hsin-Che hchiang3@mail.umd.edu

J. Gaubert, M. Egels, P. Pannier and S. Bourdel, "Design method for broadband CMOS," Electronics Letters, Vol. ?, No. ?, March 2005, pp. ?? - ??.

Dandin, Marc-Peralte mpdandin@wam.umd.edu

Y.-C. Shih and C.-Y. Wu, "A New CMOS Pixel Structure for Low-Dark-Current and Large-Array-Size Still Imager Applications," IEEE Transactions on Circuits and Systems - I, Vol. 51, No. 11, November 2004, pp. 2204 - 2214.

Ergueta, Maria mergueta@mail.umd.edu

S. Lineykin and S. Ben-Yaakov, "Analysis of Thermoelectric Coolers by a Spice-Compatible Equivalent-Circuit Model," IEEE Power Electronics Letters, Vol. 3, No. 2, June 2005, pp. 63 - 66.

Esmaili Sardari, Saeed sesmaili@mail.umd.edu

Y. H. Kang and S. Hong, "A Simple Flash Memory Cell Model for Transient Circuit Simulation," IEEE Electron Devices Letters, Vol. 26, No. 8, August 2005, pp. 563 - 565.

Gebeyehu, Netsanet A gebeyehu@glue.umd.edu

V. M. Lim, M. A. Do, J. G. Ma and K. S. Yeo, "A Broadband CMOS LNA for WLAN Applications," Proceedings of the IEEE (conference on ???), City ?, Mo.?, 2003, pp. 42 - 46.

Haas, Bryan Michael bhaas@mail.umd.edu

R. S. Tucker and D. J. Pope, "Microwave Circuit Models of Semiconductor Injection Lasers," IEEE Transactions on Microwave Theory and Techniques, Vol. MTT-31, No. 3, March 1983, pp. 289 - 294.

Haim Hoffer, Fiorella Gera fiorella@mail.umd.edu

S. Koziel and R. Schaumann, "Continuous-Time Active-RC Filter Model for Computer-Aided Design and Optimization," IEEE Transactions on Circuits and Systems - I, Vol. 52, No. 7, July 2005, pp. 1292 - 1301.

Hemmady, Sameer Dileep samydh@wam.umd.edu

K. M. Coumo and A.V. Oppenheim, "Circuit Implementation of Synchronized Chaos with Applications to Communications," Physical Review Letters, Vol. 71, No. 1, July 5, 1993, pp. 65 - 68.

Hla, Myat Htet myat_hla@yahoo.com

Rcd. 10/31/05

R. Z. Shi and T. Horiuchi, "A Summating, Exponentially-Decaying CMOS Synapse for Spiking Neural Systems," Proceedings of the 2004 IEEE Conference on Neural Information Processing Systems, city (?), pp. ???

Kim, Chan Ho chanho@mail.umd.edu

G. O. Glas and J. G. Zola, "Input Resistance of the Single-Input Active-Load Differential Amplifier," IEEE Transactions on Circuits and Systems - I, Vol. 41, No. 1, January 1994, pp. 60 - 63.

Lee, William Weiham williee@wam.umd.edu

D. Yu and S. Yuvarajan, "Modeling and Performance Studies of a Fuel-Cell Powered Boost Converter," Proceedings of the IEEE (conference on ?), City?, month?, 2004, pp. 713 - 717.

Mitchell, Gregory Allen grmitch@wam.umd.edu

W. J. Tenten and P. R. Shepherd, "Novel Analog to Digital Converters Using Voltage Inverter Switches: Part I - Reference Circuits," IEEE Transaction on Circuits and Systems, Vol. 38, No. 6, June 1991, pp. 582 - 589.

Przybyla, Colleen Jean przybyla@mail.umd.edu

C. A. Papazoglou and C. A. Karybakas, "A Transformation to Obtain CCII-Based Adjoint of Op.-Amp.-Based Circuits," IEEE Transactions on Circuits and Systems - II, Vol. 45, No. 7, July 1998, pp. 894 - 898.

Puthenpurayil, Sebastian M purayil@mail.umd.edu

A. M. Ismail and A. M. Soliman, "Novel CMOS Wide-Linear-Range Transconductance Amplifier," IEEE Transactions on Circuits and Systems - I, Vol. 47, No. 8, August 2000, pp. 1248 - 1253.

Reddy, Srinivas Tirumuru srinivasreddy3@yahoo.com

B. Nauta, "A CMOS Transconductance-C Filter Technique for Very High Frequencies," IEEE Journal of Solid-State Circuits, Vol. 27, No. 2, February 1992, pp. 142 - 153.

Reis, Alexandre amsreis@glue.umd.edu

S. Pennisi, "CMOS multiplier for grounded capacitors," Electronics Letters, Vol. 38, No. 15, July 18, 2002, pp. 785 - 786.

Sander, David Morris dsander@mail.umd.edu

T. Serrano-Gotarredona, B. Linares-Barranco, and A. G. Andreou, "A General Translinear Principle for Subthreshold MOS Transistors," IEEE Transactions on Circuits and Systems - I, Vol. 46, No. 5, May 1999, pp. 607 - 616.

Tkachuk, Sergiy STKACHUK@mail.umd.edu

J. Mahattanakul, and J. Chutichatuporn, "Design Procedure for Two-State CMOS Opamp with Flexible Noise-Power Balancing Scheme," IEEE Transactions on Circuits and Systems - I, Vol. 52, No. 8, August 2005, pp. 1508 - 1514.

Wienke, James P jpwienke@wam.umd.edu

L. Dai and R. Harjani, "CMOS Switched-Op-Amp-Based Sample-and-Hold Circuit," IEEE Journal of Solid-State Circuits, Vol. 35, No. 1, January 2000, pp. 109 - 113.

Total number of students from 0101: 22