

Paper Choices

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Hiruy Abdu	hiruy@glue.umd.edu	Z. Shi
R. F. Lyon and C. Mead, "An Analog Electronic Cochlea," IEEE Transactions on Acoustics, Speech and Signal Processing, Vol. 36, No. 7, July 1988, pp. 1119 - 1134		
Brian Conaghan	conaghan@isr.umd.edu	Luohong Liu #2
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Kareem Darwish	kareem@glue.umd.edu	Brian Conaghan
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#1. M. F. Toner and G. W. Roberts, "Towards Built-In-Self-Test for SNR Testing of a Mixed-Signal IC," Proceedings of the 1993 International Symposium on Circuits and Systems, Chicago, May 3-6, 1993, pp. 1599 - 1603. #2. M. Soma, "A Design-for-Test Methodology for Active Analog Filters," Proceedings of the IEEE 1990 International Test Conference, pp. 183 - 191		
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J. Vad der Spiegel, P. Mueller, D. Blackman, P. Chance, C. Donham, R. Etienne-Cummings and P. Kinget, "An Analgo Neural Computer with Modular Architecture for Real-Time Dynamic Computations," IEEE Journal of Solid-State Circuits, Vol. 37, No. 1, January 1992, pp. 82 - 92.		
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#1 S. Dadgupta and R. Chakrabarti, "Effect of Isonising Radiation on the Characteristics of a MOSFET," IEE Proceedins, Circuits, Devices and Systems, Vol. 147, No. 2, April 2000, pp. 133 -138. #2 L. Bisdounis, O. Koufopavlou and S. Nikolaidis, "Modelling Output Waveform and Propagation Delay of a CMOS Inverter in the Submicron Range," IEE Proceedings, Circuits, Devices and Systems, Vol. 145, No. 6, December 1998, pp. 402 - 408.		
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T. Okuda, I. Naritake, T. Sugibayashi, Y. Nakajima and T. Murotani, "A 12-ns 8-Mbyte DRAM Secondary Cache for a 64-bit Microprocessor," IEEE Journal of Solid-State Circuits, Vol. 35, No. 8, August 2000, pp. 1153 - 1158.		
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#1. J. L. Pelloie, C. Raynaud, O. Faynot, A. Grouillet, J. Du Port de Pontcharra, "CMOS Technologies for Low-Power and Low-Voltage Circuits," Microelectronic Engineering, Vol. 48, 1999, pp. 327 - 334. #2. F. Udrea, D. Garner, K. Sheng, A. Popescu, H. T. Lim and W. I. Milne, "SOI Power Devices," Electronics and Communication Engineering Journal, February 2000, pp. 27 - 40.		
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N. Yang,, W. K. Henson, J. R. Hauser and J. J. Wortman, "Modeling Study of Ultrathin Gate Oxides Using Direct Tunneling Current and Capacitance-Voltage Measurements in MOS Devices," IEEE Transactions on Electron Devices, Vol. 46, No. 7, July 1999, pp. 1464 - 1471.		
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#1 T. Morie and Y. Amemiya, "An All-Analog Expandable Neural Network LSI with On-Chip Backpropagation Learning," IEEE Journal of Solid-State Circuits, Vol. 29, No. 9, September 1994, pp. 1086 - 1092.		
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B. Mason, J. Barton, G. A. Fish, L. A. Coldren and S. P. DenBaars, "Design of Sampled Grating DBR Lasers with Integrated Semiconductor Optical Amplifiers," IEEE Photonics Technology Letters, Vol. 12, No. 7, July 2000, pp. 762 - 764.		
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#1. A. B. Dowlatabadi, "A Robust, Load-Insensitive Pad Driver," IEEE Journal of Solid-State Circuits, Vol. 35, No. 4, April 2000, pp. 600 -665.		
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