

ENEE 610 - Fall 2000 Network Theory, MW 5:00-6:15 Room CHM 0115

Paper Choices

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C.-S. Wang, S.-Y. Yuan, S.-Y. Kuo, "Full-Seing ViCMOS Schmitt Trigger," IEEE Proceedings, Circuits and Devices, Vol. 144, No. 5, October 1997, pp. 303 - 307.		
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Aneesh Aggarwal	aneesh@eng.umd.edu	Renju Thomas Renju Thomas
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T. S. Dranger and R. Primer, "Collective Process Circuit That Sorts," IEE Proceedings, Circuits, Devices, Systems, Vol. 144, No. 3, June 1997, pp. 145 - 148.		
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Y. Takefuji and K. C. Lee, "Artificial Neural Networks for Four-Coloring Map Problems and K-Colorability Problems," IEEE Transactions on Circuits and Systems, Vol. 38, No. 3, March 1991, pp. 326 - 333.		
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Y. I. Ismail, E. G. Friedman and J. L. Neves, "Dynamic and Short-Circuit Power of CMOS Gates Driving Lossless Transmission Lines," IEEE Transactions on Circuits and Systems - I, Vol. 46, No. 8, August 1999, pp. 950 - 961.		
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J. C. Sprott, "Simple Chaotic Systems and Circuits," American Journal of Physics, Vol. 68, No. 3, August 2000, pp. 758 - 763.		
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E. A. M. Klumperink, "A Systematic Approach to Circuit Design and Analysis: Classification of Two-VCCS Circuits," IEEE Transactions on Circuits and Systems - I, Vol. 46, No. 7, July 1999, pp. 810 - 819.		
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B. E. Shi, L. Gao and K. K. Lau, "A Resistor/Transconductor Network for Linear Fitting," IEEE Transactions on Circuits and Systems - II, Vol. 47, No. 4, April 2000, pp. 322 - 330.		
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J. B. Juo and H. J. Liao, "A High-Speed BiCMOS Tristate Buffer," IEEE Transactions on Circuits and Systems - II, Vol. 40, No. 7, July 1993, pp. 440 -442.		
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A. A. El-Adawy and A. M. Soliman, "A Low-Voltage Single Input Class AB Tranconductor with Rail-To-Tail Input Range," IEEE Transactions on Circuits and Systems -I, Vol. 47, No. 2, February 2000, pp. 236 - 242.		
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H. Darabi and A. A. Abidi, "A 4.5-mW 900-MHz CMOS Receiver for Wireless Paging," IEEE Journal of Solid-State Circuits, Vol. 35, No. 8, August 2000, pp. 1083 - 1096.		
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K. Arabi and B. Kaminska, "Oscillation Built-In Self Test (OBIST) Scheme for Functional and Structural Testing of Analog and Mixed-Signal Integrated Circuits," Proceedings of the IEEE International Test Conference, 1997, pp. 786 - 795.		
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H. Nakano, T. Saito and K. Mitsubori, "Synchronization from Pulse-Coupled Integrate -and Fire Chaotic Oscillators," IEICE Transactions Fundamentals, Vol. ENG-A, No. 5, May 2000, pp. 805 - 900.		
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Ji Luo	jluo@glue.umd.edu	Hu Huang Hu Huang
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William B. Mason	wbmason@wam.umd.edu	Angelo Dominguez Angelo Dominguez
M. F. Li, U. Dasgupta, X. W. Zhang and Y. C. Lim, "A Low-Voltage CMOS OTA with Rail-to-Rail Differential Input Range," IEEE Transactions on Circuits and Systems - I, Vol. 47, No. 1, January 2000, pp. 1 - 8.		
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S.-I. Liu and Y.-S. Hwang, "CMOS Squarer and Four-Quadrant Multiplier," IEEE Transactions on Circuits and Systems - I, Vol. 42, No. 2, February 1995, pp. 119 - 122.		
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D. J. Comer, M. L. Christiansen and D. K. Fox, "The Loop-Gain Modulator: A New Class of Linear Amplitude Modulators," IEEE Transactions on Circuits and Systems, Vol CAS-34, No. 10, October 1987, pp. 1161 - 1164.		
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Wipawee Siriwongpairat	wipawee@wam.umd.edu	Thanongsak Himsoon
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L. Bu and T.-D. Chiueh, "Solving the Shortest Path Problem Using an Analog Network," IEEE Transactions on Circuits and Systems, Vol. 46, No. 11, November 1999, pp. 1360 - 1363.		
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S. Paul, A. M. Schlaffer and J. A. Nossek, "Optimal Charging of Capacitors," IEEE Transactions on Circuits and Systems - I, Vol. 47, No. 7, July 2000, pp. 1009 - 1015.		
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		Woo Chul Jeon
K. Phang and D. A. Johns, "A CMOS Optical Preamplifier for Wireless Infrared Communications," IEEE Transactions on Circuits and Systems - II, Vol. 46, No. 7, July 1999, pp. 852 - 859.		
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Y.-S. Xhu and W.-K. Chen, "Low-Pass Impedance Transformation Networks," IEE Proceedings, Circuits and Devices, Vol. 144, No. 5, October 1997, pp. 384 - 388?.		
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V. C. Vincence, C. G. Montoro and M. C. Schneider, "A High-Swing MOS Cascode Bias Circuit for Operation at Any Current Level," Proceedings of the IEEE International Symposium on Circuits and Systems, Geneva, May 28-31, 2000, pp. V-489 - V-492.		