Class Exercise—From State Tables to Minimal Sums

12/2/15

The state table shown below is for a clocked synchronous sequential network. Assigning codes in binary order to the states, determine minimal sum excitation and output expressions for the sequential network assuming the use of JK flip-flops.

Table P7.19

Present state	Next state		Output (z)	
	Input (x)		Input (x)	
	0	1	0	1
A	B	C	0	0
B	A	A	0	1
C	D	A	0	1
D	A	D	0	1