# Cryptography

Lecture 15

## Announcements

- Midterm--Median grade 61 (+10 point curve)
- Extra Credit Opportunities
- Current Events and Scholarly Paper

## Agenda

- This time
- Domain Extension
  - (Merkle-Damgard) (K/L 5.2) (Review)
  - Sponge Construction
  - New topic: Practical constructions
    - Stream Ciphers (K/L 6.1)

# The Merkle-Damgard Transform

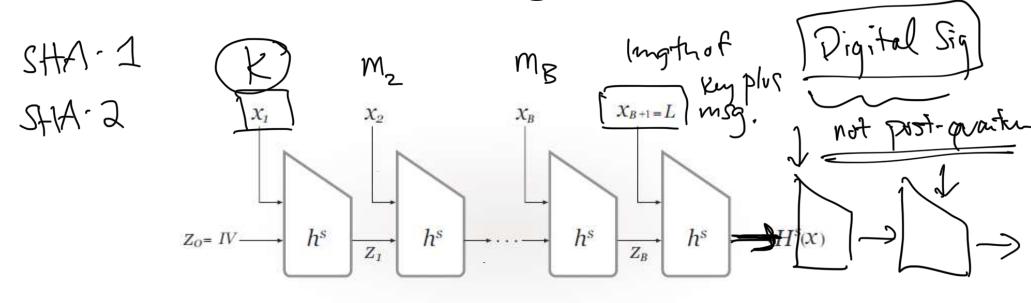


FIGURE 5.1: The Merkle-Damgård transform.

Blockchai Bitcoin

Length Extension Affack

M-D doesn't behave like a

toly random function

Proof of Work

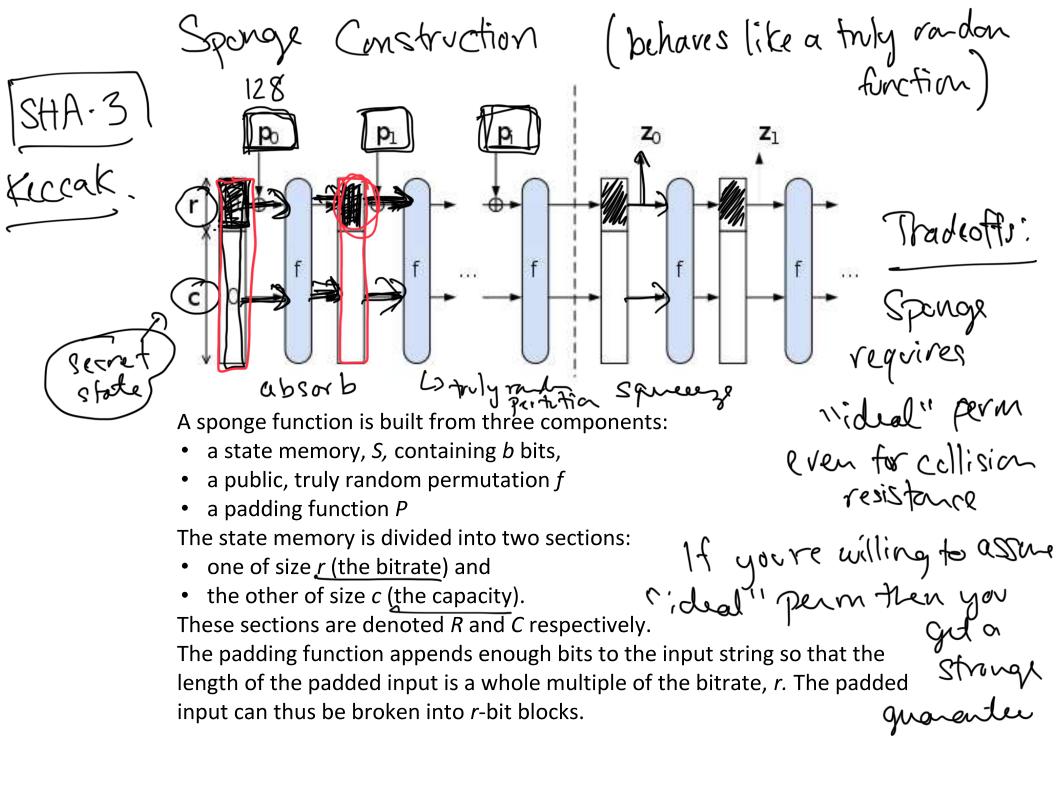
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#### **Operation**

The sponge function operates as follows:

- The state S is initialized to zero
- The input string is padded. This means the input p is transformed into blocks of r bits using the padding function P.
- R is XORed with the first r-bit block of padded input
- S is replaced by f(S)
- R is XORed with the next *r*-bit block of padded input (if any)
- S is replaced by f(S)

. . .

The process is repeated until all the blocks of the padded input string are used up ("absorbed" in the sponge metaphor).

The sponge function output is now ready to be produced ("squeezed out") as follows:

- The R portion of the state memory is the first *r* bits of output
- If more output bits are desired, S is replaced by f(S)
- The R portion of the state memory is the next r bits of output

. . .

The process is repeated until the desired number of output bits are produced. If the output length is not a multiple of r bits, it will be truncated.

## Sponge

- Is there a length-extension attack on Sponge?
- Plusses and minuses of Sponge versus Merkle Damgard.
- A Sponge-based hash known as Keccak was selected as SHA-3. Standardized by NIST in 2015.

# candidate construction Streen Ciphe = PRG.

- Consists of an array of n registers  $\vec{s} \coloneqq s_{n-1}, \dots, s_0$
- Feedback loop specified by a set of n feedback coefficients  $\vec{c} \coloneqq c_{n-1}, \dots, c_0$ .
- The size of the array is called the degree of the LFSR.
- Each register stores a single bit
- The state st of the LFSR at any point is the set of bits contained in the registers
- State of the LFSR is updated in a series of "clock ticks" by shifting the values to the right and setting the new value of the leftmost register.

## **LFSR**

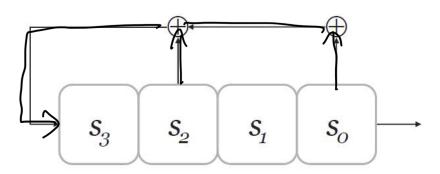


FIGURE 6.1: A linear feedback shift register.

If state in registers at time t is:

$$\vec{s}^{(t)} \coloneqq s_3^{(t)}, s_2^{(t)}, s_1^{(t)}, s_1^{(t)}$$

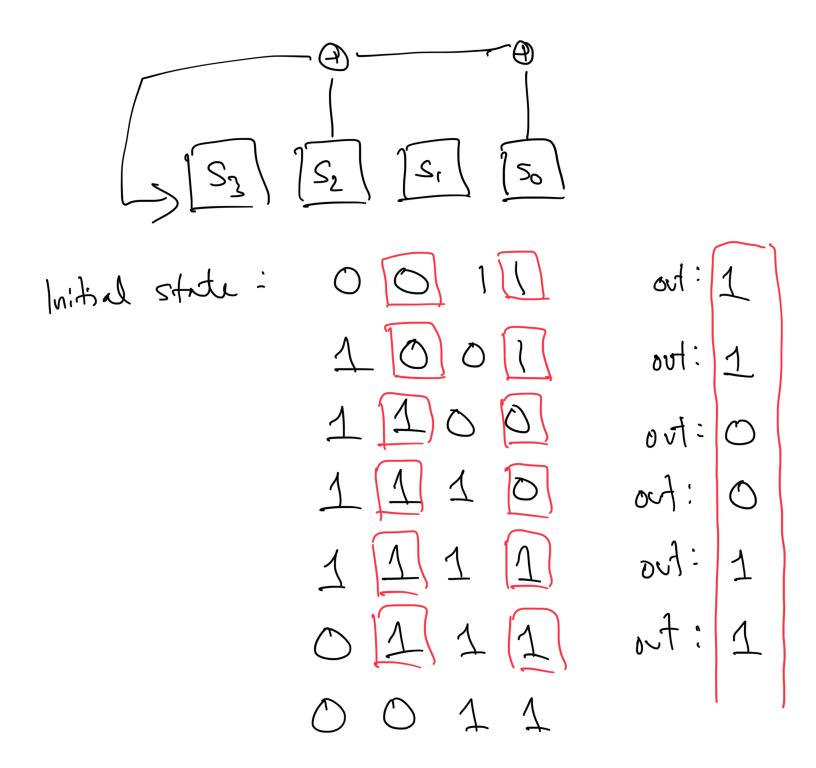
Then state in registers at time t + 1 is:

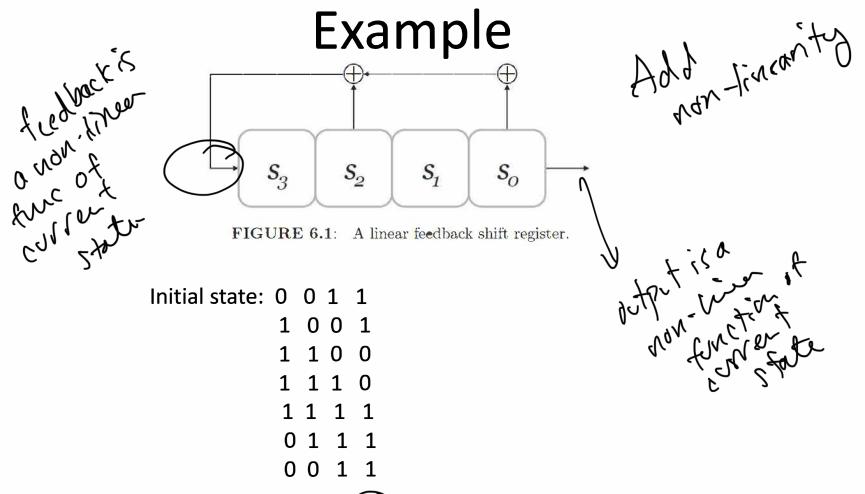
$$\underbrace{\left(s_3^{(t+1)} = \langle \vec{c}, \vec{s}^{(t)} \rangle\right)}_{S_2^{(t+1)} := s_3^{(t)}} \mod 2 .$$

$$s_1^{(t+1)} := s_2^{(t)}$$

$$s_0^{(t+1)} := s_1^{(t)}$$

$$S_{3}^{(\bullet)}S_{2}^{(\bullet)}S_{3}^{(\bullet)}$$





- LFSR can cycle through at most  $(2^n)$  states before repeating
- A maximum-length LFSR cycles through all  $2^{n-1}$  non-zero states before repeating
- Depends only on feedback coefficients, not on initial state
- Maximum-length LFSR's can be constructed efficiently

Fully break LFSR due to linearity.
First nortputs your yny give me the initial state.
yn, y, y, in yn
Yn Yn., 41 ( =
$y_{2n-2} \cdots y_{n-1} \frac{1001}{1001}$
Known fredback copt Known
Can be set up after seeing 2n outputs of CFSR Once I solve for Cny Co can predict the next output.

## Reconstruction Attacks

- LFSR are always insecure. We have the following generic attack:
- If state has n bits, then
  - First n output bits  $y_0, ..., y_{n-1}$  reveal initial state  $s_0, ..., s_{n-1}$
  - Can use next n output bits  $y_n, ..., y_{2n-1}$  to determine  $c_0, ..., c_{n-1}$  by setting up a system of n linear equations in n unknowns: